

[Product Information]

IMX477-AACK

Ver.1.0

Diagonal 7.857 mm (Type 1/2.3) 12.3 Mega-Pixel CMOS Image Sensor with Square Pixel for Color Cameras

Description

The IMX477-AACK is a diagonal 7.857 mm (Type 1/2.3) 12.3 Mega-pixel CMOS active pixel type stacked image sensor with a square pixel array. It adopts Sony's Stacked CMOS Image Sensor technology to achieve high speed image capturing by column parallel A/D converter circuits and high sensitivity and low noise image (comparing with conventional CMOS image sensor) through the backside illuminated imaging pixel structure. R, G, and B pigment primary color mosaic filter is employed. It equips an electronic shutter with variable integration time. It operates with three power supply voltages: analog 2.8 V, digital 1.05 V and 1.8 V for input/output interface and achieves low power consumption.

In addition, this product is designed for use in consumer use camcorder. When using this for another application, Sony Semiconductor Solutions Corporation does not guarantee the quality and reliability of product. Therefore, don't use this for applications other than consumer use camcorder.

In addition, individual specification change cannot be supported because this is a standard product.

Consult your Sony Semiconductor Solutions Corporation sales representative if you have any questions.

Features

- ◆ Back-illuminated and stacked CMOS image sensor
- ◆ Digital Overlap High Dynamic Range (DOL-HDR) mode with raw data output.
- ◆ High signal to noise ratio (SNR).
- ◆ Full resolution @60 frame/s (Normal), 4K2K @60 frame/s (Normal), 1080p @240 frame/s
Full resolution @40 frame/s (12 bit Normal), Full resolution @30 frame/s (DOL-HDR, 2 frame)
- ◆ Output video format of RAW12/10/8, COMP8.
- ◆ Power Save Mode
- ◆ Pixel binning readout and V sub-sampling function.
- ◆ Independent flipping and mirroring.
- ◆ Input clock frequency 6 to 27 MHz
- ◆ CSI-2 serial data output (MIPI 2lane/4lane, Max. 2.1 Gbps/lane, D-PHY spec. ver. 1.2 compliant)
- ◆ 2-wire serial communication.
- ◆ Two PLLs for independent clock generation for pixel control and data output interface.
- ◆ Defect Pixel Correction (DPC)
- ◆ Ambient Light Sensor (ALS)
- ◆ Fast mode transition. (on the fly)
- ◆ Dual sensor synchronization operation (Multi camera compatible)
- ◆ 7 k bit of OTP ROM for users.
- ◆ Built-in temperature sensor
- ◆ 10-bit/12-bit A/D conversion on chip
- ◆ 92-pin high-precision ceramic package

Sony reserves the right to change products and specifications without prior notice.

Sony logo is a registered trademark of Sony Corporation.

Device Structure

- ◆ CMOS image sensor
- ◆ Image size Diagonal 7.857 mm (Type 1/2.3)
- ◆ Total number of pixels 4072 (H) × 3176 (V) approx. 12.93 M pixels
- ◆ Number of effective pixels 4072 (H) × 3064 (V) approx. 12.47 M pixels
- ◆ Number of active pixels 4056 (H) × 3040 (V) approx. 12.33 M pixels
- ◆ Chip size 7.564 mm (H) × 5.476 mm (V)
- ◆ Unit cell size 1.55 μm (H) × 1.55 μm (V)
- ◆ Package 92 pin LGA

Image Sensor Characteristics

(Tj = 60 °C)

Item		Value	Remarks
Sensitivity (F2.8)	Min.	250 LSB	1/120 s integration
Saturation signal	Min.	1023 LSB	

Basic Drive Mode

Drive mode	Number of active pixels	Maximum frame rate [frame/s]	Output interface	ADC [bit]
Full (4:3) (Normal)	4056 (H) × 3040 (V) approx. 12.33 M pixels	60	CSI-2	10
		40	CSI-2	12
Full (4:3) (DOL-HDR)	4056 (H) × 3040 (V) approx. 12.33 M pixels	DOL 2 frame : 30 DOL 3 frame : 15	CSI-2	10
Full (16:9) 4K2K (Normal)	4056 (H) × 2288 (V) approx. 9.28 M pixels	79	CSI-2	10
Full (16:9) 4K2K (DOL-HDR)	4056 (H) × 2288 (V) approx. 9.28 M pixels	DOL 2 frame : 39 DOL 3 frame : 19	CSI-2	10
Full (4:3) Binning (Normal)	2028 (H) × 1520 (V) approx. 3.08 M pixels	179	CSI-2	10
Full (16:9) Binning 1080P (Normal)	2028 (H) × 1128 (V) approx. 2.29 M pixels	240	CSI-2	10
Full (16:9) Binning 720P (Normal)	1348 (H) × 750 (V) approx. 1.01 M pixels	240	CSI-2	10
Full (16:9) Scaling 1080P (Normal)	2024 (H) × 1142 (V) approx. 2.31 M pixels	79	CSI-2	10
Full (16:9) Scaling 720P (Normal)	1348 (H) × 762 (V) approx. 1.03 M pixels	79	CSI-2	10